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*Bo Wan; Jinian Bian;*

Solid-State and Integrated Circuit Technology, 1995 4th International Conference on , 24-28 Oct. 1995

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#### 4 Debugging of behavioral VHDL specifications by source level emulation

*Koch, G.; Kebschull, U.; Rosenstiel, W.;*

Design Automation Conference, 1995, with EURO-VHDL, Proceedings EURO-DAC '95., European , 18-22 Sept. 1995

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*Koch, G.; Kebschull, U.; Rosenstiel, W.;*

System Synthesis, 1997. Proceedings., Tenth International Symposium on , 17-19 Sept. 1997

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**1 Global events and global breakpoints in distributed systems***Haban, D.; Weigel, W.;*

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